

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Noriaki HIRAGA) Confirmation No. 7356
Application No.: 09/625,643) Group Art Unit: 2836
Filed: July 25, 2000) Examiner: Z. Kitov
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE WITH ENHANCED RESISTANCE TO ELECTROSTATIC BREAKDOWN) Mail Stop RCE)))

Commissioner for Patents U.S. Patent and Trademark Office 220 20th Street South Customer Window, **Mail Stop RCE** Crystal Plaza Two, Lobby, Room 1B03 Arlington, VA 22202

Sir:

AMENDMENT FOR ENTRY WITH RCE FILING

In response to the Final Office Action dated August 20, 2004, the period for reply to which has been extended to January 20, 2004, by a petition for a two-month extension of time filed concurrently herewith, please amend the above-identified RCE application as follows: